Electronic Acknowledgement Receipt

EFS ID:	1111638
Application Number:	10677791
Confirmation Number:	9575
Title of Invention:	Semiconductor device having voltage feedback circuit therein, and electronic apparatus using the same
First Named Inventor:	Isao yamamoto
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Filer:	Daniel P. Lent
Filer Authorized By:	
Attorney Docket Number:	PRM-0002
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment - After Non-Final Rejection	Amendment_dated_07_13_2006.pdf	458712	no	7
Warnings:					
Information:					
		Total Files Size (in bytes):	458712		

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.